

MECHANISM FOR INTEGRATING PROGRAMMABLE DEVICES INTO SOFTWARE BASED FRAMEWORKS FOR DISTRIBUTED COMPUTING

ABSTRACT OF THE DISCLOSURE

[00123] A layered mechanism for integrating programmable devices into software based frameworks for distributed processing wherein the software framework interfaces with an adaptation layer, which in turn interfaces with a programmable device, such as a field programmable gate array (FPGA). The adaptation layer specifies and enforces compatible electrical, physical and logical interfaces between the programmable device and the software-based framework of which the device, the application running on it, and the adaptation layer form a component